



DTM IMP QUESTION'S

1. Boolean eq. using basic gates & simplification
2. K-map
3. SOP & POS example & minterm & maxterm, sop to pos & pos to sop conversion
4. Number system conversion
5. T flip flop
6. SR flip-flop
7. JK flip-flop
8. universal gate, derive basic gates using NAND / NOR
9. 2's complement subtraction
10. logic gates with their types
11. D flip flop
12. Half adder
13. Compare sequential logic circuit and combinational logic circuit
14. Full adder
15. De-Morgan's law
16. compare TTL and CMOS logic families
17. Necessity of MUX
18. Half subtractor
19. Full subtractor
20. Number system Conversion